

# Zhijing Li

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## EDUCATION

<b>Cornell Univeristy</b>	Ithaca, NY
<i>Doctor of Philosophy in Electrical and Computer Engineering</i>	<i>Aug 2018 – Present</i>
<b>Shanghai Jiao Tong University</b>	Shanghai, China
University of Michigan – Shanghai Jiao Tong University Joint Institute (UM-SJTU JI)	GPA 3.5/4.0
<i>Bachelor of Science in Electrical and Computer Engineering</i>	<i>Sept 2014 – Aug 2018</i>
Exchange Student at Hong Kong University, Computer Science Department	<i>Jan 2017 – May 2017</i>
<b>Selected Courses:</b> Advanced Computer Architecture; Computer Vision; High-Level Digital Design Automation; Computer Networks; Operating Systems; The Principles of Database System; Intro to Computer Organization; Intro to data structure.	

## PUBLICATIONS AND PATENT

### Publications

Li, Zhijing, Zhao Chen, Yili Zhang, Zixin Huang, and Weikang Qian. "Simultaneous Area and Latency Optimization for Stochastic Circuits by D Flip-flop Insertion." *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* (2018).

Hu, Junjun, Zhijing Li, Meng Yang, Zixin Huang, and Weikang Qian. "A high-accuracy approximate adder with correct sign calculation." *Integration, the VLSI Journal* (2017).

### Patent

Detection for Replay Attack Based on LAP Operator. Schedule number: G06K9/00(2006.01)I. Publication number: CN 105913024A

## RESEARCH EXPERIENCE

<b>Capra, Cornell Univeristy</b>	Dec 2018 – Present
<ul style="list-style-type: none"><li>Designing quantization table for JPEG algorithm facing neural network related applications.</li></ul>	
<b>Emerging Computing Technology Laboratory, Shanghai Jiao Tong University</b>	July 2016 – May 2018
<ul style="list-style-type: none"><li>Exploited the arrangement of D-flip flop in stochastic systems with integer linear programming.</li><li>Designed and implemented experiments for the high-accuracy approximate adder.</li></ul>	

## PROFESSIONAL EXPERIENCE

<b>Intel Asia-Pacific Research and Development Ltd.</b>	Shanghai, China
<b>Software Engineering Intern</b>	<i>July 2017 – Sep 2017</i>
<ul style="list-style-type: none"><li>Implemented key value cache on client node in distributed storage system Ceph;</li><li>Designed and implemented test on the scalability of Ceph using Cetune;</li></ul>	
<b>Tompkins International (Shanghai) Co., Ltd.</b>	Shanghai, China
<b>Analytics Engineer Internship</b>	<i>Oct 2015 – Dec 2015</i>
<ul style="list-style-type: none"><li>Sorted out data with SQLite and provided clients with the optimal way of managing supply chain in China.</li><li>Take surveys on competitors for client and analyzed the cost of their deployment and supply chain.</li></ul>	

## SELECTED PROJECTS

<b>College Students Innovative Practice Plan (IPP), Shanghai Jiao Tong University</b>	Nov 2015 – Oct 2016
<ul style="list-style-type: none"><li>Organized a team to start the program “Positioning the Quad-Rotor UAV Based on SINS and GPS Kalfman Filtering”.</li><li>Designed the upper monitor with QT creator and OpenGL.</li></ul>	
<b>Participation in Research Program (PRP), Shanghai Jiao Tong University</b>	Sept 2015 – Mar 2016
<ul style="list-style-type: none"><li>Designed a system that performs face anti-spoofing on android phones with low power consumption and little latency with little cost of accuracy.</li><li>Transplanted C++ code on Windows to Android with NDK to make portable app on mobile devices.</li></ul>	

## CAMPUS ACTIVITIES

<b>Joint Institute at Shanghai Jiao Tong University</b>	Shanghai, China
<b>Teaching Assistant</b>	<i>Sept 2015 – Aug 2016</i>
<ul style="list-style-type: none"><li>Teaching Assistant of Introduction to Engineering and Politics of International Economic Relations.</li></ul>	

## HONERS and AWARDS

<ul style="list-style-type: none"><li>Fung Scholar Program Sponsorship</li></ul>	2017
<ul style="list-style-type: none"><li>Distinguished Achievement in Extracurricular Activity Award</li></ul>	2015
<ul style="list-style-type: none"><li>JI Dean's List</li></ul>	2014, 2015
<ul style="list-style-type: none"><li>“Three Good” Student (good in study, attitude and health) in SJTU</li></ul>	2015
<ul style="list-style-type: none"><li>The SJTU Outstanding Scholarship</li></ul>	2015

## SKILLS

C/C++, Python, Verilog, SQL, Java, MatLab, Haskell.